



COFC

# PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/090,481  
Patent No.: 6,781,406  
Filed: March 4, 2002  
Issued: August 24, 2004  
Inventor(s):  
Brian D. Emberling and Ewa M.  
Kubalska

Title: USING OBSERVABILITY LOGIC FOR REAL-TIME DEBUGGING OF ASICs

Examiner: Le, Don P.  
Group/Art Unit: 2819  
Atty. Dkt. No: 5181-90200

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

Name of Registered Representative

  
Signature

9/15/2004  
Date

# TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450,  
Alexandria, VA 22313-1450

Dear Sir:

**Certificate**  
**SEP 28 2004**  
**of Correction**

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. § 254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel PC Deposit Account No. 501505\5181-90200/JCH.

Respectfully submitted,



Jeffrey C. Hood  
Reg. No. 35,198

Meyertons, Hood, Kivlin, Kowert & Goetzel PC  
P.O. Box 398  
Austin, Texas 78767-0398  
(512) 853-8800

Date: 9/15/2004

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,781,406 B2

DATED: August 24, 2004

INVENTOR(S): Emberling et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In the Claims:**Column 9

Line 36, please delete "internal functional blocks and the plurality of internal" and substitute --internal functional blocks and the plurality of internal--.

## MAILING ADDRESS OF SENDER:

Jeffrey C. Hood, Esq.,  
Meyertons, Hood, Kivlin, Kowert & Goetzel PC  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,781,406 B2

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,781,406 B2

DATED: August 24, 2004

INVENTOR(S): Emberling et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In the Claims:**Column 9

Line 36, please delete "internal functional blacks and the plurality of internal" and substitute --internal functional blocks and the plurality of internal--.

## MAILING ADDRESS OF SENDER:

Jeffrey C. Hood, Esq.,  
Meyertons, Hood, Kivlin, Kowert & Goetzel PC  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,781,406 B2